# **Unit 1. Introduction**

Digital Electronic Circuits (Circuitos Electrónicos Digitales) E.T.S.I. Informática Universidad de Sevilla

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#### **Contents**

- Digital Electronic Circuits (DEC)
- Analog vs Digital
- DEC and computer curricula
- Practical Course's information



#### **Electric circuits**





#### **Electronic circuits**



#### **Electronic circuits. Transducers**

![](_page_4_Figure_1.jpeg)

## **Electronic circuits. Example**

![](_page_5_Figure_1.jpeg)

#### **Analog vs Digital**

![](_page_6_Figure_1.jpeg)

![](_page_6_Picture_2.jpeg)

## Analog

- Analog signals
  - Continuous in time
  - Can take any value in a given range

![](_page_7_Figure_4.jpeg)

![](_page_7_Picture_5.jpeg)

## Analog

- Analog signals
  - Come from the nature: light, sound, temperature, ...
  - Transducer circuits convert natural magnitudes in electrical signals: microphones, speakers, photodiodes, antennas, ...
- Analog electronic circuits
  - Process analog signals: amplifiers, filters, ...

![](_page_8_Picture_6.jpeg)

## Digital

- Digital signals
  - Change at definite times (discrete in time)
  - Only take a definite number of values (discrete values)

![](_page_9_Figure_4.jpeg)

Sequence: 0, 1, -2, -2, 1, 2, 2, -1, 1

![](_page_9_Picture_6.jpeg)

## Digital

- Digital signals
  - Obtained from conversion from analog signals through analog-todigital converters (A/D)
  - Allow a numeric treatment of information
- Digital electronic circuits
  - Process digital signals by operating with numerical data
    - Addition, product, storing, ...

![](_page_10_Picture_7.jpeg)

#### A/D and D/A conversion

![](_page_11_Figure_1.jpeg)

![](_page_11_Figure_2.jpeg)

# **Binary digital signal**

- Binary digital signal only take two values {0, 1}
  - (this is where all the 0's and 1's fuzz comes from)
- Multi-valued digital signals can be equivalently represented by binary digital signals easily
- In most cases, we use binary digital signals for convenience (more on this in next unit)

![](_page_12_Picture_5.jpeg)

# **Binary digital signal**

![](_page_13_Figure_1.jpeg)

ERSIDAD

## E.g.: analog vs digital transmission

![](_page_14_Figure_1.jpeg)

## **Digital vs Analog**

- Drawbacks
  - Slow processing compared to analog
  - Bigger circuits
  - Higher power consumption
  - A/D and D/A requiered
  - Quantization error
- Advantages
  - Much easier design and implementation
  - Transmission without loss of quality
  - Much more powerful processing
    - Compression, error detection/correction, storage, etc.
  - Homogeneous treatment of information:
    - sound, image, text, etc.

![](_page_15_Picture_14.jpeg)

## **Digital vs Analog**

![](_page_16_Figure_1.jpeg)

#### **DEC and computer curricula**

Software

Computer networks

Computers

Digital systems

Digital circuits

Analog circuits

Circuit theory

**Electronic Physics** 

#### Computer Structure (EdC)

Digital Electronic Circuits (CED)

![](_page_17_Picture_11.jpeg)

## Summary

- Electronic circuits (electronic technology) is a convenient way to solve many practical problems:
  - Detect objects and events
  - Process control
  - Signal broadcasting and processing
  - ...
- Digital electronics simplifies and allow further applications:
  - Complex data processing
  - Complex control algorithms
  - Easy and reliable data storage
  - ...
- Digital electronic technology builds the hardware where software is executed

![](_page_18_Picture_12.jpeg)

## **Practical information**

- Teaching staff
- Digital electronics
- Program
- Activities
- Methodology
- Calendar
- Assessment (evaluation)
- Resources

![](_page_19_Picture_9.jpeg)

## **Teaching Staff**

- Jorge Juan-Chico (me)
  - Theory and practical session
- David Guerrero Martos
  - Laboratories

![](_page_20_Picture_5.jpeg)

#### Program

- Part 1: Electronic circuits and logic families
  - 1. Introduction
  - 1.5. Electronic circuits and logic families
  - 2. Digital encoding
- Part 2: Combinational applications
  - 3. Combinational circuits
  - 3.5. Hardware description languages
  - 4. Combinational subsystems
  - 5. Arithmetic and logic units
- Part 3: Sequential applications
  - 6. Synchronous sequential circuits
  - 7. Sequential subsystems

![](_page_21_Picture_13.jpeg)

#### **Activities**

- On-site/on-line (~60 hours, 4 h/week)
  - Theory sessions: concepts, examples, short tests.
  - Practical sessions: problem solving.
  - Laboratory sessions: actual implementations
  - Quizes, tests
- Off-site (~90 hours, 6 h/week)
  - Study
  - Problem solving
  - Laboratory preparation
- Tutorship
  - On-site/on-line: see professor's web page

![](_page_22_Picture_12.jpeg)

# Methodology

- Theory sessions
  - Prof. explains theory concepts and problem solving methods using practical examples
  - Proposes an assignment (key exercises).
  - Proposes additional exercises from the course's collection.
- Personal (including group) work
  - Learn the theory and methods. Try to solve the assignment NOT watching other year's solutions. Ask the prof. for assistance.
- Practical sessions
  - Students ask questions about assignments or other problems.
  - Prof. gives guidance so that students finish their assignment.
- Labs
  - Students read the lab exercise an do the pre-lab work.
  - Sample circuits are implemented in lab sessions.

#### Assessment

- In-course (continuous) assessment
  - Theory and problems (80%)
    - Two blocks: combinational, sequential
    - Short quizzes with every unit (20%)
    - Two partial tests (80%) one block each
  - Labs (20%)
    - Mandatory (may skip just one)
  - Must pass both
- Final exams
  - Theory and problem exam (80%)
    - May take only the failed block during in-course assignment
  - Lab exam (20%)
  - Must pass both
  - Marks from in-course assignment are preserved until 3<sup>rd</sup> official call

![](_page_24_Picture_15.jpeg)

#### Resources

- US Virtual Teaching platform (EV) (ev.us.es)
  - Everything should be there
- Department's web page (www.dte.us.es)
  - Professors web pages (office hours, e-mails, etc.)
  - Courses backup page (in case EV go nuts)
    - Basic emergency information
- ETSII web page (www.informatica.us.es)
  - Class place and dates, exam dates, covid-19 info, etc.
- Communication
  - Official: E-mail through EV.
  - Others: check EV and web page.

You must read the Syllabus (in EV)

![](_page_25_Picture_13.jpeg)